

A stability-ensuring procedure for designing high conversion-gain frequency doublers

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This paper presents a new general procedure for designing optimum conversion gain class B FET frequency doublers. For the first time, the two key design variables, i.e. the reflection coefficients of the input and output matching networks, $\Gamma_{IMN,2f_0}$ and Γ_{OMN,f_0} , can be independently swept without risking device oscillation and hence simulator nonconvergence. This permits the designer to directly select the best parameters for maximum but insensitive conversion gain. Exemplary conversion gain contours typical for HEMTs and MESFETs are finally presented.

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